

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	1701	apic	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/10 09:22
S2	1	("20050228918").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/10 08:45
S3	331	apic and interrupt	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/10 09:43
S4	266	apic and architecture	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/10 09:20
S5	240	advanced programmable interrupt	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/10 09:20
S6	152	apic and "710"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/10 09:32
S7	4732	powerpc	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/10 09:33
S8	13	S7 and S1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/14 09:12

S9	93	apic and platform	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/10 10:55
S10	10	apic and (platform with different or various)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/10 11:19
S11	75	local near1 apic\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/10 11:19
S12	19	apic near1 architecture	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/10 11:20
S13	26	apic with compatible	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/10 11:55
S14	278	apic same interrupt	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/10 11:59
S15	70	apic and 710/260-269.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/10 14:38
S16	67	apic same interrupt\$2 and 710/260-269.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/10 12:10

S17	4	S15 and abstraction	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/10 12:12
S18	14	S15 and layer\$2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/10 14:37
S19	12000	pass\$3 near3 interrupt\$2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/10 13:11
S20	109	pass\$3 near3 interrupt\$2 same architecture\$2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/10 13:37
S21	7602	interrupt near1 controller\$2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/10 13:37
S22	6875	interrupt controller\$2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/10 15:05
S23	154	processor threads	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/10 14:24
S24	0	address decoder thread\$2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/10 14:25

S25	122	decoder thread\$2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/10 14:25
S26	16	apic register\$2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/10 14:32
S27	12	S18 and register\$2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/10 14:37
S28	25	register same apic and 710/260-269.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/10 14:39
S29	21	register same apic and 710/260-269.ccls. not S26	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/10 14:39
S30	104	interrupt controller\$2 and abstraction layer	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/10 15:12
S31	94	interrupt controller\$2 and abstraction layer and (register\$2 or memory location\$2)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/10 15:13
S32	6	interrupt controller\$2 and abstraction layer with (register\$2 or memory location\$2)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/10 15:18

S33	5168	processor core\$2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/10 15:18
S34	64	processor core\$2 same powerpc	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/10 16:06
S35	36	processor core\$2 same powerpc and interrupt	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/10 15:21
S36	6	processor core\$2 same powerpc same interrupt\$2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/10 15:18
S37	30	processor core\$2 same powerpc and interrupt not S36	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/10 15:22
S38	14	processor cores same powerpc	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/10 15:26
S39	19359	plurality near1 (register\$2 or memory location\$2)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/10 15:26
S40	714	first near1 plurality near1 (register\$2 or memory location\$2)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/10 15:26

S41	479	first plurality near1 (register\$2 or memory location\$2)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/10 15:26
S42	11	first near1 plurality near1 (register\$2 or memory location\$2) and 710/260-269.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/10 15:32
S43	4199	pass\$3 near1 interrupt\$2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/10 16:07
S44	1686	pass\$3 interrupt\$2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/10 16:07
S45	29	S43 with processors	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/10 16:15
S46	130	S43 with (processors or cores or cpu\$2)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/10 16:18
S47	31	passing (interrupts or requests) with (processors or cpu\$2 or core\$2)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/10 16:21
S48	12	apic.ti.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/10 16:21

S49	6	((("5727217") or ("5857090") or ("6247091")).PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/14 09:13
S50	12	((("6449699") or ("6820143") or ("6820174") or ("20040117592") or ("20040162946") or ("20040263519")).PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/14 09:28
S51	34145	(cpu\$2 or processor\$2) with (read\$3 or writ\$3) near4 (register\$2 or storage)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/14 09:54
S52	406	(cpu\$2 or processor\$2) with (read\$3 or writ\$3) near4 (register\$2 or storage) and 710/260-269.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/14 09:55
S53	209	(cpu\$2 or processor\$2) near2 (read\$3 or writ\$3) near4 (register\$2 or storage) and 710/260-269.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/14 09:55
S54	18	S52 and APIC	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/14 10:13
S55	4732	Powerpc	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/14 10:13
S56	2947	power pc	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/14 10:14

S57	17	power pc and 710/260-269.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/14 10:38
S58	7337	(powerpc or power pc)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/14 10:20
S59	15	(powerpc or power pc) and apic	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/14 10:20
S60	82	(powerpc or power pc) and 710/260-269.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/14 11:33
S61	780	register\$2 near2 access\$3 near4 generat\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/14 11:34
S62	16	register\$2 near2 access\$3 near4 generat\$3 and 710/260-269.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/14 11:38
S63	37	register near2 access\$3 near2 generator	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/14 11:57
S64	2383	address near2 decod\$2 near2 register\$2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/14 11:58



S65	19	address decode registers	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/14 11:59
S69	693	hardware near2 abstraction layer\$2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/14 12:04
S70	688	hardware abstraction layer\$2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/14 12:20
S71	69	hardware abstraction layer\$2 same interrupt	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/14 12:04
S72	12	hardware abstraction layer\$2 with interrupt	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/14 12:04
S73	53	hardware abstraction layer\$2 and (powerpc or power pc)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/14 12:20
S74	25	hardware abstraction layer\$2 and (powerpc or power pc) and interrupt	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/14 12:21
S75	26	hardware abstraction layer\$2 and (powerpc or power pc) and interrupt\$2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/14 12:25

S77	96	interrupt controller\$2 same device driver\$2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/14 12:25
S78	37	interrupt controller\$2 with device driver\$2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/14 12:25
S79	17	interrupt controller\$2 with device driver\$2 and 710/260-269.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/14 13:10
S80	2	address near2 decode\$2 near3 thread\$2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/14 13:11
S81	1294	(powerpc or power pc) and (multiprocessor\$2 or (multi-processor\$2) or processor cores)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/14 14:10
S82	876	(powerpc or power pc) and (multiprocessor\$2 or (multi-processor\$2) or processor cores) and register\$2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/14 14:11
S83	485	(powerpc or power pc) and (multiprocessor\$2 or (multi-processor\$2) or processor cores) and register\$2 and interrupt\$2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/14 14:11
S84	157	(powerpc or power pc) and (multiprocessor\$2 or (multi-processor\$2) or processor cores) and register\$2 and interrupt controller\$2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/14 14:19

S85	45	(powerpc or power pc) and (multiprocessor\$2 or (multi-processor\$2) or processor cores) and register\$2 and interrupt controller\$2 and "710"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/14 14:39
S87	199	710/260.ccls. and generator	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/14 14:40
S89	9	710/260.ccls. and implementation near4 layer\$2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/14 14:40
S90	2	("6189065").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/14 16:16
S91	5522	(cpu or processor\$2) with (read\$3 and writ\$3) near4 (register\$2 or memory location\$2)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/14 16:18
S92	379	(cpu or processor\$2) with (read\$3 and writ\$3) near4 (register\$2 or memory location\$2) with interrupt\$2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/14 16:24
S93	150	(cpu or processor\$2) with (read\$3 and writ\$3) near2 (register\$2 or memory location\$2) with interrupt\$2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/14 16:23
S94	0	(cpu or processor\$2) with (read\$3 and writ\$3) near2 (register\$2 or memory location\$2) with interrupt\$2 and "710"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/14 16:23

S95	53	(cpu or processor\$2) with (read\$3 and writ\$3) near2 (register\$2 or memory location\$2) with interrupt\$2 and "710"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/14 16:24
S96	39	(cpu or processor\$2) with (read\$3 and writ\$3) near4 (register\$2 or memory location\$2) with interrupt\$2 and 710/260.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/14 16:52
S97	31	(cpu or processor\$2) with read\$3 with writ\$3 near4 (register\$2 or memory location\$2) with interrupt\$2 and 710/260.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/14 17:06
S98	1	apic compliant	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/14 17:06
S99	1	interrupt controller\$2 with abstraction with implementation	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/14 17:07
S100	9	(US-20050027914-\$ or US-20050228918-\$).did. or (US-5832279-\$ or US-6738847-\$ or US-6901475-\$ or US-6339808-\$ or US-5987538-\$ or US-6711643-\$ or US-6823414-\$).did.	US-PGPUB; USPAT	ADJ	OFF	2005/11/14 19:40
S101	5	S100 and (inter-processor or interprocessor or IPI)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/14 19:41
S102	19	(on-board or on board) interrupt\$2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/15 09:14

S10 3	36	((reliabilty or (error detection)) register\$2)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/15 09:16
S10 4	5	((reliabilty or (error detection)) register\$2) same interrupt\$2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/15 09:16
S10 5	10	(US-20050027914-\$ or US-20050228918-\$).did. or (US-5832279-\$ or US-6738847-\$ or US-6901475-\$ or US-6339808-\$ or US-5987538-\$ or US-6711643-\$ or US-6823414-\$ or US-4587511-\$). did.	US-PGPUB; USPAT	ADJ	OFF	2005/11/15 09:52
S10 6	2	S105 and (powerpc or power pc)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/15 10:01
S10 7	54	(powerpc or power pc) and 710/260.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/15 10:06
S10 8	26	(powerpc or power pc) and 710/260.ccls. and (multiprocessor\$2 or multi-processor\$2)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/15 10:22
S11 0	2	("20050138252").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/15 13:50
S11 1	11	(US-20050027914-\$ or US-20050228918-\$).did. or (US-5832279-\$ or US-6738847-\$ or US-6901475-\$ or US-6339808-\$ or US-5987538-\$ or US-6711643-\$ or US-6823414-\$ or US-4587511-\$ or US-6892260-\$).did.	US-PGPUB; USPAT	ADJ	OFF	2005/11/15 15:44

S11 2	6	S111 and decode\$2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/15 15:54
S11 3	24177	address decoder\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/15 15:54
S11 4	120	address decoder\$1 and 710/260-269.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/15 17:17
S11 5	0	address decoder\$1 and 710/260-269.ccls. with register\$2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/15 15:59
S11 6	0	address decoder\$1 and 710/260-269.ccls. same register\$2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/15 15:59
S11 7	6726	address decoder\$1 same register\$2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/15 15:59
S11 8	4738	address decoder\$1 with register\$2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/15 15:59
S11 9	4738	(address decoder\$1) with register\$2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/15 15:59

S12 0	4	(address decoder\$1) with register\$2 with layer	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/15 16:00
S12 1	6	(address decoder\$1) with register\$2 same layer	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/15 16:01
S12 2	23429	read\$3 with writ\$3 near4 register\$2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/15 17:21
S12 3	263	(read\$3 with writ\$3 near4 register\$2) same (interrupt\$2 near4 process\$2 with (cpu\$2 or processor\$2 or multi-processor\$2))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/15 17:22
S12 4	62	(read\$3 with writ\$3 near4 register\$2) with (interrupt\$2 near4 process\$2 with (cpu\$2 or processor\$2 or multi-processor\$2))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/15 17:24
S12 5	25	(read\$3 with writ\$3 near4 register\$2) with (interrupt\$2 near4 process\$2 with (cpu\$2 or processor\$2 or multi-processor\$2)) and 710/260-269.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/15 17:27
S12 6	16	(read\$3 near3 writ\$3 near4 register\$2) with (interrupt\$2 near4 process\$2 with (cpu\$2 or processor\$2 or multi-processor\$2)) and 710/260-269.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/15 17:28
S12 7	3259	second near2 (set or plurality or group) near1 register\$2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/15 17:29

S12 8	1623	second near2 (set or plurality or group) register\$2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/15 17:29
S12 9	50	S127 and 710/260-269.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/15 17:34
S13 0	84	S127 same (read\$3 near2 writ\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/15 17:34
S13 1	44	S127 with (read\$3 near2 writ\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/15 17:35
S13 2	2	S127 with (read\$3 near2 writ\$3) and 710/260-269.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/15 17:43
S13 3	16464	(first (plurality near2 registers) or (register set)) and (second (plurality near2 registers) or (register set))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/15 17:44
S13 4	286	(first (plurality near2 registers) or (register set)) and (second (plurality near2 registers) or (register set)) and 710/260-269.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/15 17:46
S13 5	286	(first (plurality near2 registers) or (register set)) same (second (plurality near2 registers) or (register set)) and 710/260-269.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/15 17:46



S13 6	286	((first (plurality near2 registers) or (register set))) and (second (plurality near2 registers) or (register set))) and 710/260-269.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/15 17:47
S13 7	179	(first ((plurality near1 register2) or (register set))) and (second ((plurality near1 register\$2) or (register set)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/15 17:48
S13 8	8	(first ((plurality near1 register2) or (register set))) and (second ((plurality near1 register\$2) or (register set))) and 710/260-269.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/15 17:52
S13 9	37	(abstraction adj layer) same (implementation near2 layer)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2005/11/15 17:52